IN THE CLAIMS

Please amend the claims as follows.

For the Examiner's convenience, a list of all claims is included below.

1. (Previously Presented) A computer system comprising:

a hardware unit to transmit data representing graphics to another computer or a display;

a processor coupled to the hardware unit; and

a storage device coupled to the processor and having stored therein a routine, which when executing by the processor, causes the processor to generate the data, the routine at least causing the processor to at least,

access a first data operand having a data element;

access a second packed data operand having at least two data elements;

insert the data element in the first data operand into a destination field of a destination register, wherein the storage device further comprises a packing device for packing integer data into the data elements.

2. (Original) The computer system of claim 1 wherein the storage device further comprises a packing device for packing floating point data into the data elements.

3. (Canceled)

4. (Previously Presented) A computer system comprising:

a hardware unit to transmit data representing graphics to another computer or a display;

a processor coupled to the hardware unit; and

a storage device coupled to the processor and having stored therein a routine, which when executing by the processor, causes the processor to generate the data, the routine at least causing the processor to at least,

access a first packed data operand having at least two data elements; and

extract one of the data elements from the first packed data operand into a field of a destination register, while preserving other data elements in the first packed data operand.

5. (Original) The computer system of claim 4 wherein the storage device further causes the processor to extract one of the data elements from the first packed data operand into a field of a packed destination register.

6. (Original) The computer system of claim 4 wherein the storage device further comprises a packing device for packing floating point data into the data elements.

7. (Original) The computer system of claim 4 wherein the storage device further comprises a packing device for packing integer data into the data elements.

8. (Previously Presented) A method comprising the computer-implemented operations that include:

decoding a single instruction;

in response to decoding the single instruction,

accessing a first data operand having a data element;

accessing a second packed data operand having at least two data elements;

inserting the data element in the first data operand into a destination field of a destination register, including packing integer data into the data elements.

9. (Previously Presented) The method of claim 8 further comprising:

packing floating point data into the data elements.

10. (Canceled)

11. (Previously Presented) A method comprising the computer-implemented operations of:

decoding a single instruction;

in response to decoding the single instruction,

accessing a first packed data operand having at least two data elements; and

extracting one of the data elements from the first packed data operand into a field of a destination register, while reserving other data elements in the first packed data operand.

12. (Previously Presented) The method of claim 11 wherein extracting one of the data elements from the first packed operand comprises extracting one of the data elements from the first packed data operand into a field of a packed destination register.

13. (Previously Presented) The method of claim 11 further comprising packing floating point data into the data elements.

14. (Previously Presented) The method of claim 11 further comprising packing integer data into the data elements.

15. (Previously Presented) A method comprising the computer implemented operations of:

accessing data representative of a first three-dimensional image;

altering the data using three-dimensional geometry to generate a second three-dimensional image, altering at least including,

accessing a first data operand having a data element;

accessing a second packed data operand having at least two data elements;

inserting the data element in the first data operand into a destination field of a destination register; and

displaying the second three-dimensional image.

16. (Previously Presented) The method of claim 15 wherein altering includes the performance of a three-dimensional transformation.

17. (Previously Presented) The method of claim 15 wherein altering includes packing floating point data into the data elements.

18. (Previously Presented) The method of claim 15 wherein altering includes packing integer data into the data elements.

19. (Previously Presented) A method comprising the computer implemented operations of:

accessing data representative of a first three-dimensional image;

altering the data using three-dimensional geometry to generate a second three-dimensional image, at least including,

accessing a first packed data operand having at least two data elements; and

extracting one of the data elements from the first packed data operand into a field of a destination register; and

displaying the second three-dimensional image.

20. (Previously Presented) The method of claim 19 wherein altering further includes extracting one of the data elements from the first packed data operand into a field of a packed destination register.

21. (Previously Presented) The method of claim 19 wherein altering includes the performance of a three-dimensional transformation.

22. (Previously Presented) The method of claim 19 wherein altering includes packing floating point data into the data elements.

23. (Previously Presented) The method of claim 19 wherein altering includes packing integer data into the data elements.